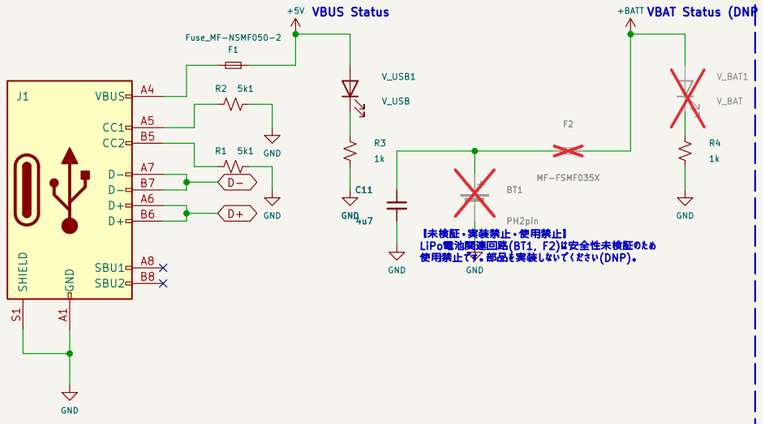
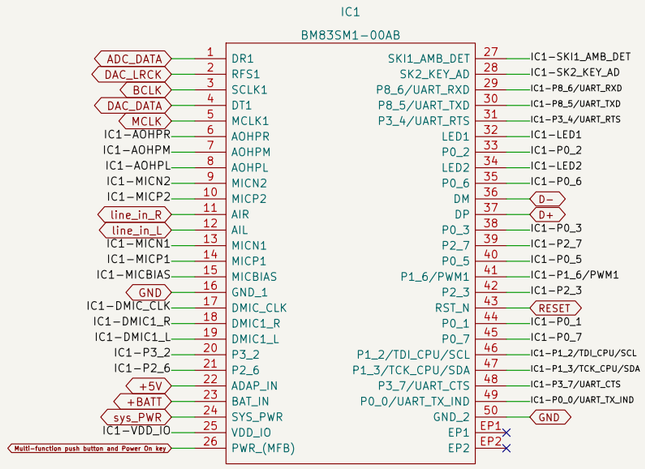
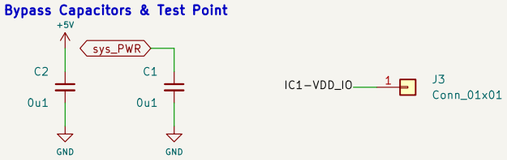
POWER SUPPLY (USB 5V ONLY)



BM83 BLUETOOTH MODULE

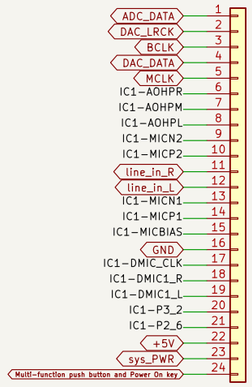


Bypass Capacitors & Test Point

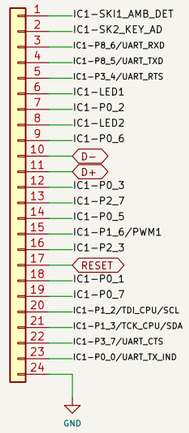


EXPANSION HEADERS (J2, J4)

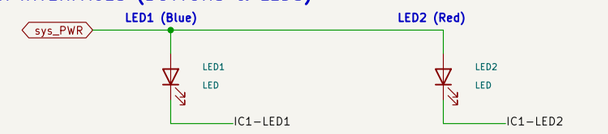
J2: Left Header (Pins 1 – 24)

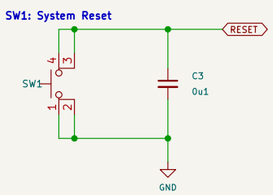


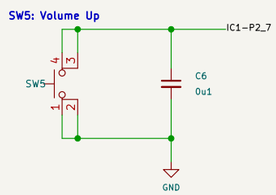
J4: Right Header (Pins 27 – 50)

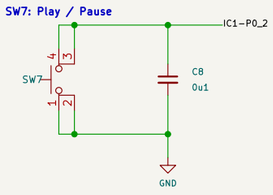


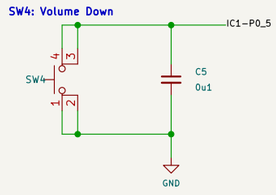
USER INTERFACES (BUTTONS & LEDS)

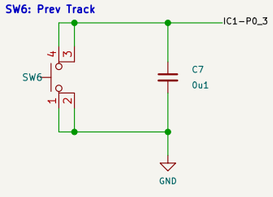


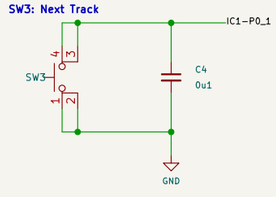












[I2S Digital Audio Interface]

- MCLK1 : Master Clock (BM83 Output)
- SCLK1 : Serial / Bit Clock (BM83 In/Out)
- DR1 : Receive Data (BM83 Input)
- RFS1 : Frame Sync / LRCK (BM83 In/Out)
- DT1 : Transmit Data (BM83 Output)

Sampling: 8, 16, 44.1, 48, 88.2, 96 kHz (16/24-bit)

[System Pin Configurations]

- P0_0 : Ext Codec Reset / Wake-up Host MCU
- P0_1 : Forward / Next Track Button
- P0_2 : Play / Pause Button
- P0_3 : Reverse / Prev Track Button
- P0_5 : Volume Down Button
- P2_7 : Volume Up Button
- P3_2 : AUX_IN DETECT

Organized into functional zones (Power, UI, BM83, Headers)
Microchip BM83 Bluetooth Audio Module Reference Schematic
YE-BM83 DevKit Project

Sheet: /
File: YE-BM83_devkit.kicad_sch

Title: YE-BM83 Development Board

Size: A3	Date: 2026-10-09	**Rev: Rev 1.0**
KiCad E.D.A. 10.0.6		Id: 1/1